

Hitoshi YAMAMOTO et al., S.N. 10/816,063  
Page 2

Dkt. 2271/71538

### Listing of Claims

The following listing of claims will replace all prior versions, and listings, of claims in the subject application:

1. (currently amended) A PC card control apparatus for controlling connections of PC cards with a host computer, comprising:

a PC card connector configured to provide connections for connecting one of a first PC card compliant with ~~specific card standards~~ a predetermined card standard and a card-adapting card for connecting a second PC card compliant with a different card standard to the PC card control apparatus,

said predetermined card standard requiring a first data format and said different card standard requiring a second data format different from said first data format;

a card detector configured to detect connection of the card-adapting card to the PC card control apparatus and to subsequently output a detection signal; and

an interconnection switching circuit configured to switch the connections of the PC card connector to connect the PC card connector to a bus interface of said host computer dedicated to ~~the second card~~ communicate data in said second data format, upon receiving the detection signal from the card detector,

wherein ~~said specific card standards require a first data format and said different card standard requires a second data format different from said first data format, and said data is communicated between~~ said bus interface ~~dedicated to~~ of said host computer and the second PC card ~~operates with in~~ operates with in said second data format of said different card standard without converting

Hitoshi YAMAMOTO et al., S.N. 10/816,063  
Page 3

Dkt. 2271/71538

to said first data format of said ~~specific card standards~~ predetermined card standard.

2. (currently amended) The PC card control apparatus according to Claim 1, wherein the ~~specific card standards~~ predetermined card standard include a PCMCIA standard.

3. (original) The PC card control apparatus according to Claim 1, wherein the second PC card is compatible with one of a USB2.0 bus interface and a PCIexpress bus interface.

4. (original) The PC card control apparatus according to Claim 1, wherein the interconnection switching circuit includes an analog switch.

5. (original) The PC card control apparatus according to Claim 1, wherein the interconnection switching circuit includes a USB hub.

6. (original) The PC card control apparatus according to Claim 1, further comprising:  
a power supply voltage switching circuit configured to switch power supply voltages including first and second power supply voltages supplied to the PC card connector based on the detection signal.

7. (original) The PC card control apparatus according to Claim 6, wherein the first power supply voltage is 3.3 volts and the second power supply voltage is 5 volts.

Hitoshi YAMAMOTO et al., S.N. 10/816,063  
Page 4

Dkt. 2271/71538

8. (original) The PC card control apparatus according to Claim 6, wherein the power supply voltage switching circuit comprises:

a power switching portion configured to issue a power supply control signal based on the detection signal; and

a first power switch configured to output to the PC card connector the first power supply voltage indicated by the power supply control signal from the power switching portion.

9. (original) The PC card control apparatus according to Claim 8, wherein the power switching portion of the power supply voltage switching circuit is mounted on the PC card control apparatus and the first power switch is provided outside the PC card control apparatus.

10. (original) The PC card control apparatus according to Claim 8, further comprising:

a second power switch configured to be activated upon insertion of the second PC card after receiving the first power supply voltage from the first power switch and to output in a predetermined time period a signal informing the first power supply voltage becomes stable.

11. (original) The PC card control apparatus according to Claim 1, wherein amongst components of the PC card control apparatus at least the card detector, the interconnection switching circuit, and the power switching portion are integrated into a one-chip IC.

12. (currently amended) A PC card control apparatus for controlling connections of PC cards with a host computer, comprising:

Hitoshi YAMAMOTO et al., S.N. 10/816,063  
Page 5

Dkt. 2271/71538

providing means for providing connections for connecting one of a first PC card compliant with ~~specific card standards~~ a predetermined card standard and a card-adapting card for connecting a second PC card compliant with a different card standard to the PC card control apparatus,

said predetermined card standard requiring a first data format and said different card standard requiring a second data format different from said first data format;

detecting means for detecting insertion of the card-adapting card in the PC card control apparatus and subsequently outputting a detection signal; and

switching means for switching the connections provided by the providing means to connect the card-adapting card to a bus interface of said host computer dedicated to ~~the second PC card~~ communicate data in said second data format, upon receiving the detection signal from the detecting means,

wherein ~~said specific card standards require a first data format and said different card standard requires a second data format different from said first data format, and said data is communicated between~~ said bus interface ~~dedicated to~~ of said host computer and the second PC card ~~operates with in~~ said second data format of said different card standard without converting to said first data format of said ~~specific card standards~~ predetermined card standard.

13. (currently amended) The PC card control apparatus according to Claim 12, wherein the ~~specific card standards~~ predetermined card standard include a PCMCIA standard.

14. (original) The PC card control apparatus according to Claim 12, wherein the second

Hitoshi YAMAMOTO et al., S.N. 10/816,063  
Page 6

Dkt. 2271/71538

PC card is compatible with one of a USB2.0 bus interface and a PCIexpress bus interface.

15. (original) The PC card control apparatus according to Claim 12, wherein the switching means includes an analog switch.

16. (original) The PC card control apparatus according to Claim 12, wherein the switching means includes a USB hub.

17. (original) The PC card control apparatus according to Claim 12, further comprising:  
altering means for altering power supply voltages including first and second power supply voltages supplied to the connections provided by the providing means based on the detection signal.

18. (original) The PC card control apparatus according to Claim 17, wherein the first power supply voltage is 3.3 volts and the second power supply voltage is 5 volts.

19. (original) The PC card control apparatus according to Claim 17, wherein the altering means comprises:

issuing means for issuing a power supply control signal based on the detection signal; and  
sending means for sending to the connections the first power supply voltage indicated by the power supply control signal from the issuing means.

Hitoshi YAMAMOTO et al., S.N. 10/816,063  
Page 7

Dkt. 2271/71538

20. (original) The PC card control apparatus according to Claim 19, wherein the issuing means of the altering means is mounted on the PC card control apparatus and the sending means is provided outside the PC card control apparatus.

21. (original) The PC card control apparatus according to Claim 19, further comprising:  
outputting means for operating upon insertion of the second PC card after receiving the first power supply voltage from the sending means and outputting in a predetermined time period a signal informing the first power supply voltage becomes stable.

22. (original) The PC card control apparatus according to Claim 12, wherein amongst components of the PC card control apparatus at least the detecting means, the switching means, and the issuing means are integrated into a one-chip IC.

23. (currently amended) A method ~~[[of]]~~ for controlling connections of first and second PC cards to a host computer, the method comprising the steps of:

providing a PC card connector having connections for connecting one of a first PC card compliant with ~~specific card standards~~ a predetermined card standard and a card-adapting card having a connection for connecting a second PC card compliant with a different card standard to a PC card control apparatus,

said predetermined card standard requiring a first data format and said different card standard requiring a second data format different from said first data format;

detecting insertion of the card-adapting card in the PC card control apparatus;

Hitoshi YAMAMOTO et al., S.N. 10/816,063  
Page 8

Dkt. 2271/71538

outputting a detection signal upon a time the detecting step detects the insertion of the card-adapting card; and

switching the connections of the PC card connector to connect the PC card connector to a bus interface of said host computer dedicated to ~~the second PC card~~ communicate data in said second data format, upon receiving the detection signal output by the outputting step,

wherein ~~said specific card standards require a first data format and said different card standard requires a second data format different from said first data format~~, and said data is communicated between said bus interface dedicated to of said host computer and the second PC card operate with in said second data format of said different card standard without converting to said first data format of said ~~specific card standards~~ predetermined card standard.

24. (currently amended) The method according to Claim 23, wherein the ~~specific card standards~~ predetermined card standard include a PCMCIA standard.

25. (original) The method according to Claim 23, wherein the second PC card is compatible with one of a USB2.0 bus interface and a PCIexpress bus interface.

26. (original) The method according to Claim 23, wherein the switching step switches the connections with an analog switch.

27. (original) The method according to Claim 23, wherein the switching step switches the connections with a USB hub.

Hitoshi YAMAMOTO et al., S.N. 10/816,063  
Page 9

Dkt. 2271/71538

28. (original) The method according to Claim 23, the method further comprising the steps of:

altering power supply voltages including first and second power supply voltages supplied to the PC card connector based on the detection signal.

29. (original) The method according to Claim 28, wherein the first power supply voltage is 3.3 volts and the second power supply voltage is 5 volts.

30. (original) The method according to Claim 28, wherein the altering step comprises the steps of:

issuing a power supply control signal based on the detection signal; and

sending the first power supply voltage indicated by the power supply control signal issued by the issuing step.

31. (original) The method according to Claim 30, wherein the issuing step issues a power supply control signal in the PC card control apparatus and the sending step sends the first power supply voltage from outside of the PC card control apparatus.

32. (original) The method according to Claim 30, the method further comprising the steps of:

waiting for insertion of the second PC card in the card-adapting card; and

Hitoshi YAMAMOTO et al., S.N. 10/816,063  
Page 10

Dkt. 2271/71538

outputting, in a predetermined time period after receiving the first power supply voltage, a signal informing the first power supply voltage becomes stable.

33. (original) The method according to Claim 23, wherein components of the PC card control apparatus, except for a component related to a power supply, are integrated into a one-chip IC.

34. (currently amended) A PC card control apparatus for controlling connections of PC cards with a host computer, comprising:

a PC card connector configured to provide connections for connecting one of a first PC card compliant with ~~specific card standards~~ a predetermined card standard and a second PC card compliant with a different card standard to the PC card control apparatus,

said predetermined card standard requiring a first data format and said different card standard requiring a second data format different from said first data format;

a card detector configured to detect insertion of the second PC card in the PC card control apparatus and to subsequently output a detection signal; and

an interconnection switching circuit configured to switch the connections of the PC card connector to connect the PC card connector to a bus interface of said host computer dedicated to ~~the second PC card~~ communicate data in said second data format, upon receiving the detection signal from the card detector,

~~wherein said specific card standards require a first data format and said different card standard requires a second data format different from said first data format, and said data is~~

Hitoshi YAMAMOTO et al., S.N. 10/816,063  
Page 11

Dkt. 2271/71538

~~communicated between~~ said bus interface ~~dedicated to~~ of said host computer and the second PC card operate with in said second data format of said different card standard without converting to said first data format of said ~~specific card standards~~ predetermined card standard.

35. (currently amended) The PC card control apparatus according to Claim 34, wherein ~~the specific card standards~~ predetermined card standard include a PCMCIA standard.

36. (original) The PC card control apparatus according to Claim 34, wherein the second PC card is compatible with one of a USB2.0 bus interface and a PCIexpress bus interface.

37. (original) The PC card control apparatus according to Claim 34, wherein the interconnection switching circuit includes an analog switch.

38. (original) The PC card control apparatus according to Claim 34, wherein the interconnection switching circuit includes a USB hub.

39. (original) The PC card control apparatus according to Claim 34, further comprising:  
a power supply voltage switching circuit configured to switch power supply voltages including first and second power supply voltages supplied to the PC card connector based on the detection signal.

40. (original) The PC card control apparatus according to Claim 39, wherein the first

Hitoshi YAMAMOTO et al., S.N. 10/816,063  
Page 12

Dkt. 2271/71538

power supply voltage is 3.3 volts and the second power supply voltage is 5 volts.

41. (original) The PC card control apparatus according to Claim 39, wherein the power supply voltage switching circuit comprises:

a power switching portion configured to issue a power supply control signal based on the detection signal; and

a first power switch configured to output to the PC card connector the first power supply voltage indicated by the power supply control signal from the power switching portion.

42. (original) The PC card control apparatus according to Claim 41, wherein the power switching portion of the power supply voltage switching circuit is mounted on the PC card control apparatus and the first power switch is provided outside the PC card control apparatus.

43. (original) The PC card control apparatus according to Claim 34, wherein amongst components of the PC card control apparatus, at least the card detector, the interconnection switching circuit, and the power switching portion are integrated into a one-chip IC.

44. (currently amended) A PC card control apparatus for controlling connections of PC cards with a host computer, comprising:

providing means for providing connections for connecting one of a first PC card compliant with ~~specific card standards~~ a predetermined card standard and a second PC card compliant with a different card standard to the PC card control apparatus,

Hitoshi YAMAMOTO et al., S.N. 10/816,063  
Page 13

Dkt. 2271/71538

said predetermined card standard requiring a first data format and said different card standard requiring a second data format different from said first data format;

detecting means for detecting attachment of the second PC card to the PC card control apparatus and subsequently outputting a detection signal; and

switching means for switching the connections provided by the providing means to connect the card-adapting card to a bus interface of said host computer dedicated to the ~~second PC card~~ communicate data in said second data format, upon receiving the detection signal from the detecting means,

wherein ~~said specific card standards require a first data format and said different card standard requires a second data format different from said first data format, and~~ said data is communicated between said bus interface ~~dedicated to~~ of said host computer and the second PC card ~~operates with~~ in said second data format of said different card standard without converting to said first data format of said ~~specific card standards~~ predetermined card standard.

45. (currently amended) The PC card control apparatus according to Claim 44, wherein the ~~specific card standards~~ predetermined card standard include a PCMCIA standard.

46. (original) The PC card control apparatus according to Claim 44, wherein the second PC card is compatible with one of a USB2.0 bus interface and a PCIexpress bus interface.

47. (original) The PC card control apparatus according to Claim 44, wherein the switching means includes an analog switch.

Hitoshi YAMAMOTO et al., S.N. 10/816,063  
Page 14

Dkt. 2271/71538

48. (original) The PC card control apparatus according to Claim 44, wherein the switching means includes a USB hub.

49. (original) The PC card control apparatus according to Claim 44, further comprising:  
altering means for altering power supply voltages including first and second power supply voltages supplied to the connections provided by the providing means based on the detection signal.

50. (original) The PC card control apparatus according to Claim 49, wherein the first power supply voltage is 3.3 volts and the second power supply voltage is 5 volts.

51. (original) The PC card control apparatus according to Claim 49, wherein the altering means comprises:

issuing means for issuing a power supply control signal based on the detection signal; and  
sending means for sending to the connections the first power supply voltage indicated by the power supply control signal from the issuing means.

52. (original) The PC card control apparatus according to Claim 51, wherein the issuing means of the altering means is mounted on the PC card control apparatus and the sending means is provided outside the PC card control apparatus.

Hitoshi YAMAMOTO et al., S.N. 10/816,063  
Page 15

Dkt. 2271/71538

53. (original) The PC card control apparatus according to Claim 44, wherein amongst components of the PC card control apparatus at least the detecting means, the switching means, and the issuing means are integrated into a one-chip IC.

54. (currently amended) A method ~~[[of]]~~ for controlling connections of first and second PC cards to a host computer, the method comprising the steps of:

providing a PC card connector having connections for connecting one of a first PC card compliant with ~~specific card standards~~ a predetermined card standard and a second PC card compliant with a different card standard to a PC card control apparatus,

said predetermined card standard requiring a first data format and said different card standard requiring a second data format different from said first data format;

detecting connection of the second PC card to the PC card control apparatus;

outputting a detection signal upon a time the detecting step detects the connection of the second PC card; and

switching the connections of the PC card connector to connect the PC card connector to a bus interface: of said host computer dedicated to the second PC card communicate data in said second data format, upon receiving the detection signal output by the outputting step,

wherein ~~said specific card standards require a first data format and said different card standard requires a second data format different from said first data format, and said data is communicated between~~ said bus interface ~~dedicated to~~ of said host computer and the second PC card operates with in said second data format of said different card standard without converting to said first data format of said ~~specific card standards~~ predetermined card standard.

Hitoshi YAMAMOTO et al., S.N. 10/816,063  
Page 16

Dkt. 2271/71538

55. (currently amended) The method according to Claim 54, wherein the ~~specific card~~  
~~standards~~ predetermined card standard include a PCMCIA standard.

56. (original) The method according to Claim 54, wherein the second PC card is  
compatible with one of a USB2.0 bus interface and a PCIexpress bus interface.

57. (original) The method according to Claim 54, wherein the switching step switches  
the connections with an analog switch.

58. (original) The method according to Claim 54, wherein the switching step switches  
the connections with a USB hub.

59. (original) The method according to Claim 54, the method further comprising the  
steps of:

altering power supply voltages including first and second power supply voltages supplied  
to the PC card connector based on the detection signal.

60. (original) The method according to Claim 59, wherein the first power supply voltage  
is 3.3 volts and the second power supply voltage is 5 volts.

61. (original) The method according to Claim 59, wherein the altering step comprises

Hitoshi YAMAMOTO et al., S.N. 10/816,063  
Page 17

Dkt. 2271/71538

the steps of:

issuing a power supply control signal based on the detection signal; and  
sending the first power supply voltage indicated by the power supply control signal  
issued by the issuing step.

62. (original) The method according to Claim 61, wherein the issuing step issues a power supply control signal in the PC card control apparatus and the sending step sends the first power supply voltage from outside of the PC card control apparatus.

63. (original) The method according to Claim 54, wherein components of the PC card control apparatus, except for a component related to a power supply, are integrated into a one-chip IC.

64. (currently amended) A passive-card-adapting card, comprising:  
a first card connector configured to be compatible with ~~specific card standards~~ a predetermined card standard for a first PC card and to connect the passive-card-adapting card to a PC card control apparatus;  
a second card connector configured to have a connection for connecting a second PC card compliant with a card standard different from the ~~specific card standards~~ predetermined card standard for the first PC card; and  
a regulator configured to regulate a power supply voltage supplied by the PC card control apparatus in order to adapt the power supply voltage for the second PC card.

Hitoshi YAMAMOTO et al., S.N. 10/816,063  
Page 18

Dkt. 2271/71538

65. (currently amended) The passive-card-adapting card according to Claim 64, wherein the ~~first PC card standards~~ predetermined card standard include a PCMCIA standard.

66. (original) The passive-card-adapting card according to Claim 64, wherein the second PC card is compatible with one of a USB2.0 bus interface and a PCIexpress bus interface.

67. (original) The passive-card-adapting card according to Claim 64, further comprising:  
a power switch configured to be activated upon insertion of the second PC card after receiving the power supply voltage from the regulator and to output in a predetermined time period a signal informing the power supply voltage becomes stable.

68. (currently amended) A passive-card-adapting card, comprising:  
connecting means for being compatible with ~~specific card standards~~ a predetermined card standard for a first PC card and connecting the passive-card-adapting card to a PC card control apparatus;

providing means for providing a connection for connecting a second PC card compliant with a card standard different from the ~~specific card standards~~ predetermined card standard for the first PC card; and

regulating means for regulating a power supply voltage supplied by the PC card control apparatus in order to adapt the power supply voltage for the second PC card.

Hitoshi YAMAMOTO et al., S.N. 10/816,063  
Page 19

Dkt. 2271/71538

69. (currently amended) The passive-card-adapting card according to Claim 68, wherein the ~~first PC card standards~~ predetermined card standard include a PCMCIA standard.

70. (original) The passive-card-adapting card according to Claim 68, wherein the second PC card is compatible with one of a USB2.0 bus interface and a PCIexpress bus interface.

71. (original) The passive-card-adapting card according to Claim 68, further comprising:  
outputting means for operating upon insertion of the second PC card after receiving the power supply voltage from the regulating means and outputting in a predetermined time period a signal informing the power supply voltage becomes stable.

72. (currently amended) A method of controlling a passive-card-adapting card, comprising the steps of:

providing a connection for connecting the passive-card-adapting card to a PC card control apparatus, the passive-card-adapting card being compatible with ~~specific card standards~~ a predetermined card standard for a first PC card;

regulating a power supply voltage supplied by the PC card control apparatus in order to adapt the power supply voltage for a second PC card compliant with a card standard different from the ~~specific card standards~~ predetermined card standard for the first PC card; and

providing a connection for connecting the second PC card.

73. (currently amended) The method according to Claim 72, wherein the ~~first PC card~~

Hitoshi YAMAMOTO et al., S.N. 10/816,063  
Page 20

Dkt. 2271/71538

~~standards~~ predetermined card standard include a PCMCIA standard.

74. (original) The method according to Claim 72, wherein the second PC card is compatible with one of a USB2.0 bus interface and a PCIexpress bus interface.

75. (original) The method according to Claim 72, the method further comprising the steps of:

waiting for connection of the second PC card to the passive-card-adapting card; and  
outputting, in a predetermined time period after receiving the power supply voltage, a signal informing the power supply voltage becomes stable.

76. (previously presented) The PC card control apparatus of claim 1, wherein said bus interface is internal to a host system.

77. (currently amended) The PC card control apparatus of claim 1, wherein said second PC card is not compliant with the ~~specific card standards~~ predetermined card standard, and said card-adapting card enables said second PC card not compliant with the ~~specific card standards~~ predetermined card standard to connect to the PC card control apparatus.